**Table 1:** Parallel Signal Assignments

| Std. Name | Use In TTL_PAPERS | I/O Address, Mask |
|---|---|---|
| Strobe | P_NAK, not interrupt ack | `P_PORTBASE`+2, 0x01 |
| D0 | P_D0, bit 0 of output nybble | `P_PORTBASE`+0, 0x01 |
| D1 | P_D1, bit 1 of output nybble | `P_PORTBASE`+0, 0x02 |
| D2 | P_D2, bit 2 of output nybble | `P_PORTBASE`+0, 0x04 |
| D3 | P_D3, bit 3 of output nybble | `P_PORTBASE`+0, 0x08 |
| D4 | P_LG, light LED Green | `P_PORTBASE`+0, 0x10 |
| D5 | P_LR, light LED Red | `P_PORTBASE`+0, 0x20 |
| D6 | P_S0, strobe P_RDY to 0 | `P_PORTBASE`+0, 0x40 |
| D7 | P_S1, strobe P_RDY to 1 | `P_PORTBASE`+0, 0x80 |
| Ack | P_I3, bit 3 of input nybble | `P_PORTBASE`+1, 0x40 |
| Busy | P_RDY, toggling ready signal | `P_PORTBASE`+1, 0x80 |
| PE | P_I2, bit 2 of input nybble | `P_PORTBASE`+1, 0x20 |
| SlctIn | P_I1, bit 1 of input nybble | `P_PORTBASE`+1, 0x10 |
| AutoFD | P_IRQ, interrupt request | `P_PORTBASE`+2, 0x02 |
| Error | P_I0, bit 0 of input nybble | `P_PORTBASE`+1, 0x08 |
| Init | P_SEL, int/ready select | `P_PORTBASE`+2, 0x04 |
| Slct | *reserved* | `P_PORTBASE`+2, 0x08 |