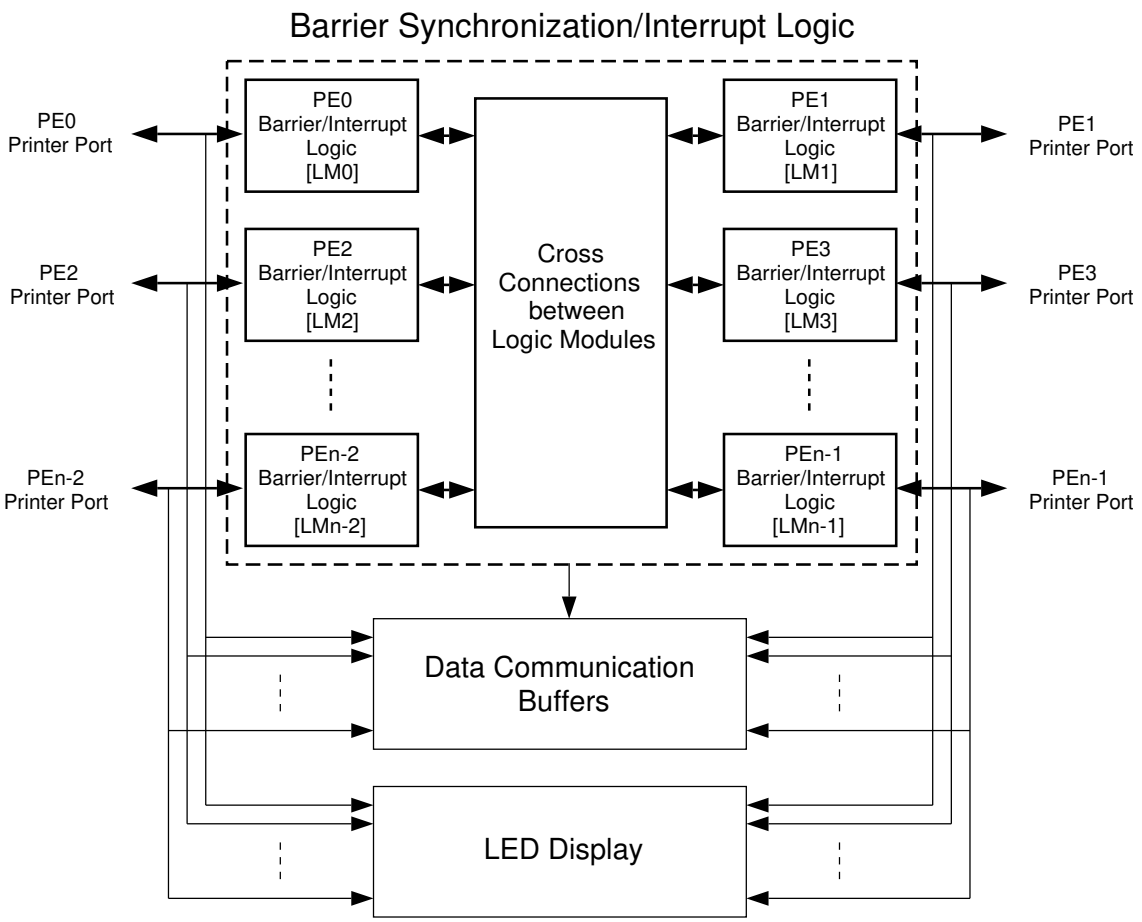
Barrier Synchronization/Interrupt Logic
PE0
Printer Port
PE2
Printer Port
PEn-2
Printer Port
PE0
Barrier/Interrupt
Logic
[LM0]
PE2
Barrier/Interrupt
Logic
[LM2]
PEn-2
Barrier/Interrupt
Logic
[LMn-2]
Cross
Connections
between
Logic Modules
PE1
Barrier/Interrupt
Logic
[LM1]
PE3
Barrier/Interrupt
Logic
[LM3]
PEn-1
Barrier/Interrupt
Logic
[LMn-1]
PE1
Printer Port
PE3
Printer Port
PEn-1
Printer Port
Data Communication
Buffers
LED Display