

Reference Information

Feel free to separate (and discard) this sheet from your exam.
It is intended as a reference only.

MIPS Instructions (a subset)

add	r1,r2,r3
addi	r1,r2,immediate
addu	r1,r2,r3
and	r1,r2,r3
andi	r1,r2,immediate
or	r1,r2,r3
ori	r1,r2,immediate
sub	r1,r2,r3
subi	r1,r2,immediate
xor	r1,r2,r3
xori	r1,r2,immediate
lui	r1,immediate
slt	r1,r2,r3
beq	r1,r2,label
bne	r1,r2,label
j	label
jal	label
jr	r1
lw	r1,offset(r2)
sw	r1,offset(r2)

Single-Cycle Implementation Architecture

